



2811

501.39082X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): UNO, et al.

Serial No.: 09/646,671

Filed: September 20, 2000

For: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

Group: Not yet assigned

Examiner: Not yet assigned

TECHNOLOGY CENTER 2800

JAN 22 2003

RECEIVED

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97 & 1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

January 21, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find copies of documents that were recently discovered by Applicants, which might be relevant to the subject matter of the above-identified application. Also enclosed is a Form, substantially equivalent to Form PTO-1449, listing the enclosed documents.

This Information Disclosure Statement is being submitted prior to a first Office Action on the merits in the above-identified application. Accordingly, requirements of 37 CFR § 1.97(b) are satisfied.

In any event, the following statement is made under 37 CFR § 1.97(e)(2):

No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned in light of reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

To the extent that the enclosed documents are not in English, it is respectfully submitted that requirements of 37 CFR § 1.98(a)(3) are satisfied, in connection with the enclosed Japanese Patent Document No. 2001-110789, by U.S. Patent No. 6,340,435 corresponding to the Japanese patent document and the English translation enclosed therewith, and English language Abstracts of other Japanese language documents (that is, WO 00/14786; Japanese Patent Document No. 7-94483; Japanese Patent Documents No. 8-8231 and No. 8-17928.

In view of all of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR § 1.97 and 1.98 have been satisfied, in connection with all documents listed on the enclosed Form. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR § 1.136. Please charge any shortage in fees due in connection with the filing

of this paper, including extension of time fees, to the Deposit Account No. 01-2135
(Case No. 501.39082X00) and please credit any excess fees to such Deposit
Account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

A handwritten signature in black ink, appearing to read "Gregory E. Montone". The signature is fluid and cursive, with the first letters of the first and last names being capitalized and prominent.

Gregory E. Montone
Registration No. 28,141

1300 North Seventeenth Street
Suite 1800
Arlington, VA 22209
Tel.: 703-312-6600
Fax.: 703-312-6666
GEM/sjg

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DKT. NO.

501.39082X00

SERIAL NO.

09/646,671

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(Use several sheets if necessary)

APPLICANT

UNO, et al.

FILING DATE

September 20, 2000

GROUP

TBD

U.S. PATENT DOCUMENTS

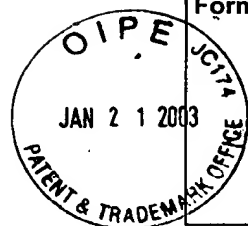
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA	2002/0054962	05/09/02	Huang			
AB	2001/0055873	12/27/01	Watanabe et al.			
AC	6,340,435	01/22/02	Bjorkman et al.			
AD	6,043,145	3/28/00	Suzuki et al.			
AE	5,889,330	03/30/99	Nishimura et al.			
AF	5,763,954	6/09/98	Hyakutake			
AG	5,728,630	03/17/98	Nishimura et al.			
AH	5,604,380	02/18/97	Nishimura et al.			

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AI	11-40543	02/12/99	Japan			Abstract	
AJ	2001-60584	3/6/01	Japan			X	
AK	WO 99/21217	4/29/99	PCT				
AL	WO 99/52135	10/14/99	PCT				
AM	WO 00/14786	3/16/00	PCT			X	
AN	EP 1 120 822	01/08/02	EP				
AO	7-94483	4/7/95	Japan			Abstract	
AP	8-8231	01/12/96	Japan			Abstract	
AQ	8-17928	1/19/96	Japan			Abstract	
AR	2001-110789	4/20/01	Japan			X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AS	C.Y. Chang et al., "ULSI Technology", Chapter 8, pages 418-421, McGraw Hill, 1996
AT	Shyam P. Murarka et al., "Copper-Fundamental Mechanisms for Microelectronic Applications", Chapter 6, pages 175-201, John Wiley & Sons, 2000
Examiner	Date Considered



RECEIVED
 JAN 22 2003
 TECHNOLOGY CENTER 2800